RAJEEV GANDHI MEMORIAL COLLEGE OF ENGINEERING & TECHNOLOGY (AUTONOMOUS)

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

WEB OF SCIENCE (SCI/SCIE/ESCI) INDEXED JOURNALS

AY: 2017-18

1. C. V. S. Reddy, C. Venkataiah, V.R.Kumar, et al. Design and Simulation of CNT Based Nano-Transistor for Greenhouse Gas Detection Journal of Nanoelectronics and Optoelectronics Vol. 12, pp. 1–9, 2017.

2. T.JayachandraPrasad, Chennakesavulu et al. "Improved Performance of Error controlling codes using Pass Transistor logic", CSSP, Springer, DOI: 10.1007/S00034-017-0596-4, July-2017. [I.F=1.694]

3. C.Venkataiah, K. Satyaprasad, T. Jayachandra Prasad, "Signal integrity analysis for coupled SWCNT interconnects using stable recursive algorithm", Microelectronics Journal (Elsevier), volume. 74, pp. 13-23, 2018.

4. C.Venkataiah, K. Satyaprasad, T. Jayachandra Prasad, "FDTD algorithm to achieve absolute stability in performance analysis of SWCNT interconnects", Journal of computational electronics (Springer), 2018. DOI: 10.1007/s10825-017-1125-1.

5. C.Venkataiah, K. Satyaprasad, T. Jayachandra Prasad, "Crosstalk induced performance analysis of single walled carbon nanotube interconnects using stable finite difference time domain model", Journal of nanoelectronics and optoelectronics (American scientific publishers), Vol. 12, pp. 1-10, 2017.

6. C.Venkataiah, K. Satyaprasad, T. Jayachandra Prasad, "Insertion of optimal number of repeaters in pipelined nano interconnects for transient delay minimization", Circuit systems and signal processing (Springer), June, 2018. DOI: 10.1007/s00034-018-0876-7.